# FL: A modified and Combined Approach for Page Replacement in Operating Systems

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*Abstract* - Operating system executes processes by page replacement, which is one of the basic requirements of any operating system. There has been continuous and tremendous study towards the design of algorithms that have minimum number of page faults. Variety of page replacement algorithms have been proposed and applied in different situations, in which FIFO (first in first out) and LRU (least recently used) are two of the most popular page replacement policies.

Modern operating systems strive for throughput maximization by reducing page faults. As page fault rate decreases, efficiency of an algorithm increases, due to the fact that operating system will be busy more in execution of processes, rather than doing I/O.

In the given literature, we propose FL approach, the combination of FIFO and LRU, is such a page replacement technique, which has led to equal or a smaller number of page faults than FIFO and LRU alone. Specifically, this algorithm works well in conditions, if a smaller number of memory frames are available. Conducting tests with a different reference strings and different number of frames, the page fault rate is examined.

*Index Terms*—Efficiency, FIFO, FL, LRU, Memory frames, Page Faults, Page replacement, Reference strings.

#### I. INTRODUCTION

Memory management is one of the important tasks of the operating system. Situations may arise, where there are limited memory frames available, to accommodate the pages of processes [1]. It is desirable that process should execute with high throughput in these limited frames [1]. For a processes' page to execute, its valid bit must be set in the page table. There are basic FIFO, Optimal and LRU page replacement policies available to achieve this purpose [2]. These techniques are tested on the grounds of particular reference strings. After simulating the algorithms on the given reference strings, the algorithm that has minimum number of page faults is said to be the best one. As per the research work carried out by [3],

LRU is better than FIFO page replacement algorithm. It is generally observed that, as the number of frames increases, page fault rate decreases [4].

FIFO and LRU page replacement algorithms are practically possible, whereas optimal algorithm is not, as it requires the future knowledge of the page, which is to arrive [5].

In the literature [6] a reference string has been used, to test these algorithms. It is:

test these algorithms. It is:																			
7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1																			
1. FIFO Page Replacement Algorithm																			
7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1
→7 	7 → 0	7 - 0 → 1	) 2 0 1	2 0 -	2 3 1 -	2 - 3 )	→ 4 3 0	4 → 2 0 -	4 · 2 → 3	→ 0 2 3	0 2 3	0 2 3	0 → 1 3 -	0 1 2	0 1 2	0 - 1 2	7 1 → 2	7 0 2 -	7 0
*	*	*	*		*	*	*	*	*	*			*	*			*	*	*
Total Page Faults = 15																			
Tot	al l	Pag	ge l	Fau	ılts	=	15												
Tot 2.	al l LR	Pag U	ge l (I	Fau Lea	ılts .st	= R	15 ece	entl	ly	Us	sed	)	Pa	ge	R	epl	lace	em	ent
Tot 2.	al ] LR Alg	Pag LU gor	ge l (I rith	Fau Lea m	ılts .st	= R	15 ece	entl	y	Us	sed	)	Pa	ge	R	epl	ace	em	ent
Tot 2. 7	al ] LR Alg	Pag U gor	ge I (I ith 2	Fau Lea m	ılts .st 3	= R(	15 ece 4	entl 2	ly 3	Us 0	sed	) 2	Pag 1	ge 2	R 0	epl 1	lace	em 0	ent
Tot 2. $7 \rightarrow 7$	al ] LR Als	Pag CU gor 1 $7 + \frac{1}{2}$	ge I (I ith 2 2 0 1	Fau Lea m 0 2 0 1	$\frac{1}{3}$	= R 0 2 -	15 ecce 4 $4$ $3$	$\begin{array}{c} 2\\ 4\\ 0\\ 2\\ 2\\ 2\\ 2\end{array}$	$\mathbf{y}$	$U_{5}$ 0 $\rightarrow 0$ 3 2	3 0 3 2	) 2 0 3 2	$Pa_{2}$ $1$ $\frac{1}{3}$ $\frac{1}{2}$	2 1 3 2	R 0 1 2	epl 1 1 2	ace $7$ $1$ $0$ $-7$ $7$	em	1 7 0

Total Page Faults = 12

The above two algorithms as already proved, are useful.

### II. MOTIVATION

The objectives behind the design of FL approach are as follows:

• Page fault rate should be as less as possible. FIFO showed 15 and LRU showed 12.

- Once the frames are fully occupied by the pages, it becomes necessary to use the best approach for page replacement.
- Achieve better CPU and memory utilization, thereby increasing throughput of our system.
- Design more sophisticated algorithms by modifying ٠ existing algorithms and testing them on different systems.

# **III. PROPOSED METHODOLOGY**

In this paper, the researcher has proposed the combined approach to page replacement. The technique is very simple to understand. It applies FIFO for first page, LRU for the second page, again FIFO for the third page and LRU for the fourth page so on, till the reference string does not end. It means that, the algorithm alternates between the sequences of FIFO and LRU approach.

The researcher has proposed two methods for applying FL approach, namely FL-1 and FL-2. The pseudocode for both the methods is below:

## A. FL-1 TECHNIQUE

Step 1: Ir	nput Reference String	• Case 4: Choose FIF
Step 2: Ir	itialize variable total_page_faults = $0$	As soon as all the fra
Step 3: R	epeat steps 4 to 16 while page $\neq$ NULL	replace an existing
Step 4: R	ead page from reference string	Page no. 7 by page 1
Step 5:	Check whether frames are empty	• Case 5: Page no. 0
	If empty_frames() then	occurs.
Step 6:	Use any frame for current page	• Case 6: Choose LR
	fill_frames()	(Replace page no. 1
Step 7:	Otherwise, if the frames are not empty	• Case 7: Page no. 0 a
Step 8:	Repeat steps 9 to 15 for FL-1 approach	occurs.
Step 9:	If valid bit is set	• Case 8: Choose FIF
Step 10:	Use the page	The previous frame
Step 11:	Otherwise, proceed to replace page as	3 and as per FIFO a
per		no. 1 (replace page
	FIFO	• Case 9: Choose LR
Replace t	the page, as per the previous	(Replace page no. 3
	allocation of LRU	• Repeat this proce
Step 12:	total_page_faults = total_page_faults	reference string con
+ 1		
Step 13:	Read the next page	By using this technique
Step 14:	If valid bit is set	reference string, the p
Step 15:	Use the page	algorithm is less than l
Step 16:	Otherwise, proceed to replace page as	FL-1 is more reliable for
per		B. FL-2 TECHNI
	LRU	Step 1: Input Reference
total_pag	$e_faults = total_page_faults + 1$	Step 2: Initialize variab

[End of Step 5 if structure] [End of Step 3 loop] Step 17: Output total\_page\_faults Step 18: Exit

Not	Note: $A = any, F = FIFO, L = LRU,$																		
$\rightarrow$ = symbol used for FIFO and LRU replacement																			
Α	Α	Α	F		L		F	L	F				L		F		L		F
7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1
→7	7 → 0	7 - 0 → 1	2 0 1	2 0 1 →	2 0 3	2 0 3	→4 0 3.	4 - 0 → 2	→ 3 0 2	3 0 2	3 0 2	3 0 2	3 1 2	3 1 2	3 1 → 0	3 1 0	7 1 0	7 1 -	7 1 0
*	*	*	*		*		*	*	*				*		*		*		
Tot	al I	Pag	e F	aul	ts :	= 1	1 (	les	s th	an	FI	FO	an	d L	LRI	J)			

The above algorithm is tested and run on the grounds of the same reference string. It works as follows:

- Case 1, Case 2 and Case 3: Initially, when the frames are empty, it does not matter, which algorithm you use for page replacement. FIFO and LRU both will allocate in the same manner (till frame allocation for page no. 7, 0 and 1), indicated by "A".
- FO approach, indicated by " $\rightarrow$ F". ames get occupied and we need to page with a new page (replace no. 2).
- already exists. So, no page fault
- U approach, indicated by " $\rightarrow$ L". by page no. 3).
- lready exists. Again, no page fault
- FO approach, indicated by " $\rightarrow$ F". allocated by LRU was frame no. approach, the next frame is frame no. 2 by page no. 4).
- U approach, indicated by " $\rightarrow$ L". by page no. 2).
- ss again and again, until the npletes.

e, it is observed that, for a given bage fault rate for the proposed FIFO and LRU. This proves that or the given reference string.

IOUE e String

Step 2: Initialize variable total\_page\_faults = 0

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- Step 3: Repeat steps 4 to 16 while page  $\neq$  NULL
- Step 4: Read page from reference string

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Step 5:	Check whether frames are empty
	If empty_frames() then
Step 6:	Use any frame for current page]
	fill_frames()
Step 7:	Otherwise, if the frames are not empty
Step 8:	Repeat steps 9 to 15 for FL-1 approach
Step 9:	If valid bit is set
Step 10:	Use the page
Step 11:	Otherwise, proceed to replace page as
per	
	FIEO

#### FIFO

Replace the page, as per the previous allocation of FIFO

Step 12:	total_page_faults = total_page_faults
+ 1	
Step 13:	Read the next page
Step 14:	If valid bit is set
Step 15:	Use the page
Step 16:	Otherwise, proceed to replace page
	as
	per LRU
	total_page_faults= total_page_faults + 1
	[End of Step 5 if structure]
	[End of Step 3 loop]
Step 17:	Output total_page_faults

Step 18: Exit

No	te:	A	= a	ny	, F	= ]	FIF	Ю,	L	= ]	LR	U,							
$\rightarrow$	= s	ym	ıbo	l u	sec	l fo	or F	FIF	0	rep	olac	en	ner	ıt					
$\leftrightarrow$	= s	ym	ıbo	l u	sec	1 fc	or I	R	Ur	ep	lac	em	en	t					
Α	Α	Α	F		L		F			L			F	L	F		L		
7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1
→ <u>7</u> 	7 ) 0	7 · 0 → 1	→ 2 0 1	2 0 1 <del>(</del>	2 0 → 3	2 0 -	2 4 3	2 4 3	3 4 2	3 → 0 2	3 0 2	3 0 2 -	3 0 ← 1	3 → 2 1	→ 0 2 1	0 2 1	0 → 7 1	0 7 1	( 7 1
*	*	÷	÷		÷		÷			÷			÷	÷	÷		*		

Total Page Faults = 11 (less than FIFO and LRU) The above algorithm is tested and run on the grounds of the same reference string.

It works as follows:

- Case 1, Case 2 and Case 3: Initially, when the frames are empty, it does not matter, which algorithm you use for page replacement. FIFO and LRU both will allocate in the same manner (till frame allocation for page no. 7, 0 and 1), indicated by "A".
- Case 5: Page no. 0 already exists. So, no page fault occurs.

- Case 6: Choose LRU approach, indicated by "↔L". (Replace page no. 1 by page no. 3).
- Case 7: Page no. 0 already exists. Again, no page fault occurs.
- Case 8: Choose FIFO approach, indicated by "→F". Here, the strategy is to replace the page in next frame using counter variable, maintained by FIFO approach. The previous frame allocated was frame no. 2. So, (Replace page no. 0 by page no. 4).
- Case 9: Choose LRU approach, indicated by "↔L". (Replace page no. 4 by page no. 0).
- Repeat this process again and again, until the reference string completes.

Again, it is observed that, the page fault rate for second technique is also less than FIFO and LRU. This also proves that FL-2 is more reliable for the given reference string.

The researcher has tested the algorithm under various situations and conditions. In some cases, LRU or FIFO shows more faults while in other cases, FL approach shows more page faults. As the prediction of page faults for FIFO and LRU is difficult, so is the case with FL-1 and FL-2 approach. All depends on the reference strings. But, as per the results of simulations performed by testing various reference strings, it was observed that many of them shows less page faults for FL approach.

# C. RESULTS AND ANALYSIS

The researcher performed 90 simulations by assuming 9 different reference strings [6] [7] and 3, 4, 5, 6, and 7 frames. Based on tests performed on different reference strings, following results were observed:



# String 2. 1, 2, 3, 4, 5, 3, 4, 1, 6, 7, 8, 7, 8, 9, 7, 8, 9, 5, 4, 5, 4, 2

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String 7. 1, 3, 4, 4, 3, 2, 1, 7, 5, 6, 4, 2, 1, 2, 4, 3, 2, 1



String 8. 6, 1, 0, 2, 2, 5, 0, 1, 4, 2, 3, 0, 7, 4, 5, 6, 0, 1



String 9. 2, 3, 1, 2, 4, 6, 0, 2, 6, 3, 2, 9, 8, 3, 6, 2, 3, 2



The above results can be summarized from the below given table. The table shows success rates for equal or less page faults.

Herein, a "√'	' represents	that a	corresponding	event	was
observed.					

Success		Fail	lure	Go	od	Average			
FL-1	FL-2	FL-	FL-	FL-	FL-	FL-	FL-		
		1	2	1	2	1	2		
√.						V			
v V									
v V							V		
۰ ا	م					1	•		
1	•		V		1	Ń			
V		V	•	V	•	•			
1		,	V	J					
1			,	V					
v	V		L			N	N		
2	v	v	2		2	v	v		
N			N	N	v				
2	2		v	1					
2	1			v		2	2		
2	2					2	2		
N	N		2	2	2	V	V		
N	2		N	N	N	2			
N	N				2	N			
N		al	N		N	N			
-1	N	N		N					
N	N						N		
N	N			N			Ň		
N	N				N	N			
N			Ň		N	Ň			
N	N			N					
N	N				1	N	N		
N	N				N	N			
	N	N			N	N			
N			N		N				
N	N					N			
N	N					N	N		
L	N	N				N	N		
, , , , , , , , , , , , , , , , , , , ,	,		$^{\vee}$		N				
N	N								
	$\checkmark$								
			$\checkmark$	$\checkmark$					
			$\checkmark$						

The above table summarizes that, out of total 90 simulations performed using FL-1 and FL-2, 66 times the page fault rate is either equal or less than FIFO and LRU, whereas 24 times it is more than FIFO and LRU. It means, FL-1 and FL-2 approach attain 73.34% success

rates compared to FIFO and LRU, while the failure rate is 26.66%. The above table also summarizes that, 29 times, the page fault rate is less than FIFO and LRU, while 37 times it is more than FL and LRU. It means, the FL approach outperforms 32.22% times better than FIFO and LRU by giving less page faults. Also, it performs well by giving 41.11% average success rate due equal number of page faults as in FIFO and LRU. Only, in some cases (26.66 %) of the times, the page faults for our algorithm are higher.

During initial stages if the number of frames is large (more free frames list available in the main memory) and the pages in the reference string are unique, FL approach behaves like FIFO. So, the page fault rate is almost equal to that of FIFO. It was specifically observed that algorithm works in a very unique manner, when the number of frames is less. So, the proposed FL approach to page replacement can be used in the situations of memory constraint. Observing the simulations, the problem with FL approach remained the same, as that in FIFO and LRU. Sometimes the victim page replaced by the new page is such that, it just arrived in the memory frame. As per the rules, the recent page should not be removed from the main memory as its execution might be required in the near future. But this condition cannot be avoided, until we take an optimal decision, which is difficult.

#### IV. CONCLUSION

This paper has two, page replacement policies FL-1 and FL-2 that can be implemented in the operating systems. After performing several simulations, they have been proved to be useful. The page fault rate can be controlled and accordingly thrashing can be avoided, thereby giving high system performance. Future work is open to take optimal decisions about deciding the next allocation for page in the available frames, calculating the time complexity of the algorithms, devising techniques to implement this approach and simulating them on various hardware and software platforms.

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